

WHAT IS CLAIMED IS:

1. A frequency-shift-keyed (FSK) receiver comprising:
 - demodulation circuitry capable of receiving an incoming signal and generating therefrom a baseband signal comprising amplitude modulated symbol stream of Logic 0 symbols and logic 1 symbols having a data rate, R ;
 - auto-correlation circuitry capable of sampling said baseband signal S times during each symbol and generating an auto-correlation function comprising a sample stream of N -bit samples having a data rate, SxR , and having positive-going peaks approximately coinciding with the center of the Logic 1 symbol 010 sequence in said baseband signal and negative-going peaks approximately coinciding with the center of the Logic 0 symbols in a 101 sequence in said baseband signal; and
 - decision circuitry capable of receiving said auto-correlation function and deciding a logic level of a first symbol function of: 1) a comparison of a signal level of a center sample of said first symbol and a mean signal level of said auto-correlation function; and 2) a comparison of said signal level of said center sample of said first symbol and a signal level of a center sample of a second symbol preceding said first symbol.

1 2. The FSK receiver as set forth in Claim 1 wherein said
2 decision circuitry further decides said logic level of said first
3 symbol as a function of a comparison of said signal level of said
4 center sample of said first symbol and a signal level of a center
5 sample of a third symbol following said first symbol.

1 3. The FSK receiver as set forth in Claim 2 wherein said
2 decision circuitry is further capable of deciding a logic level
3 of said second symbol as a function of: 1) a comparison of said
4 signal level of said center sample of said second symbol and said
5 mean signal level of said auto-correlation function and 2) a
6 comparison of said signal level of said center sample of said
7 second symbol and a signal level of a center sample of a fourth
8 symbol preceding said second symbol.

1 4. The FSK receiver as set forth in Claim 3 wherein said
2 decision circuitry further decides said logic level of said
3 second symbol as a function of a comparison of said signal level
4 of said center sample of said second symbol and said signal level
5 of said center sample of said first symbol.

1 5. The FSK receiver as set forth in Claim 4 wherein said
2 decision circuitry is further capable of deciding a logic level
3 of said third symbol as a function of: 1) a comparison of said
4 signal level of said center sample of said third symbol and said
5 mean signal level of said auto-correlation function and 2) a
6 comparison of said signal level of said center sample of said
7 third symbol and a signal level of a center sample of a fifth
8 symbol following said third symbol.

1 6. The FSK receiver as set forth in Claim 5 wherein said
2 decision circuitry further decides said logic level of said third
3 symbol as a function of a comparison of said signal level of said
4 center sample of said third symbol and said signal level of said
5 center sample of said first symbol.

1 7. The FSK receiver as set forth in Claim 6 wherein said
2 decision circuitry, in response to a determination that said
3 second symbol, said first symbol, and said third symbol comprise
4 a 010 sequence of symbols, compares said signal level of said
5 center sample of said first symbol with 1) a signal level of a
6 preceding sample of said first symbol and 2) a signal level of
7 a following sample of said first symbol to thereby determine a
8 location of a first positive-going peak of said auto-correlation
9 function corresponding to said first symbol.

P9 P8 P7 P6 P5 P4 P3 P2 P1

1 8. The FSK receiver as set forth in Claim 7 wherein said
2 decision circuitry, in response to a determination that said
3 first positive-going peak does not coincide with said center
4 sample of said first symbol, is capable of one of advancing or
5 delaying sampling of said baseband signal by at least one sample
6 time period.

1 9. The FSK receiver as set forth in Claim 6 wherein said
2 decision circuitry, in response to a determination that said
3 second symbol, said first symbol, and said third symbol comprise
4 a 101 sequence of symbols, compares said signal level of said
5 center sample of said first symbol with 1) a signal level of a
6 preceding sample of said first symbol and 2) a signal level of
7 a following sample of said first symbol to thereby determine a
8 location of a first negative-going peak of said auto-correlation
9 function corresponding to said first symbol.

1 10. The FSK receiver as set forth in Claim 9 wherein said
2 decision circuitry, in response to a determination that said
3 first negative-going peak does not coincide with said center
4 sample of said first symbol, is capable of one of advancing or
5 delaying sampling of said baseband signal by at least one sample
6 time period.

1 11. A method of processing a received frequency-shift-keyed
2 (FSK) signal comprising the steps of:

3 demodulating the incoming FSK signal and generating
4 therefrom a baseband signal comprising an amplitude modulated
5 symbol stream of Logic 0 symbols and Logic 1 symbols having a
6 data rate, R;

7 sampling the baseband signal S times during each symbol
8 and generating an auto-correlation function comprising a sample
9 stream of N-bit samples having a data rate, SxR, and having
10 positive-going peaks approximately coinciding with the center of
11 the Logic 1 symbol in a 010 sequence in the baseband signal and
12 negative-going peaks approximately coinciding with the center of
13 the Logic 0 symbols in a 101 sequence in the baseband signal;

14 deciding a logic level of a first symbol as a function
15 of: 1) a comparison of a signal level of a center sample of the
16 first symbol and a mean signal level of the auto-correlation
17 function; and 2) a comparison of the signal level of the center
18 sample of the first symbol and a signal level of a center sample
19 of a second symbol preceding the first symbol.

1 12. The method of processing the received FSK signal as set
2 forth in Claim 11 wherein the step of deciding the logic level
3 of the first symbol further comprises the sub-step of deciding
4 the logic level of the first symbol as a function of a comparison
5 of the signal level of the center sample of the first symbol and
6 a signal level of a center sample of a third symbol following the
7 first symbol.

1 13. The method of processing the received FSK signal as set
2 forth in Claim 12 further comprising the step of deciding a logic
3 level of the second symbol as a function of: 1) a comparison of
4 the signal level of the center sample of the second symbol and
5 the mean signal level of the auto-correlation function and 2) a
6 comparison of the signal level of the center sample of the second
7 symbol and a signal level of a center sample of a fourth symbol
8 preceding the second symbol.

1 14. The method of processing the received FSK signal as set
2 forth in Claim 13 wherein the step of deciding the logic level
3 of the second symbol further comprises the sub-step of deciding
4 the logic level of the second symbol as a function of a
5 comparison of the signal level of the center sample of the second
6 symbol and the signal level of the center sample of the first
7 symbol.

1 15. The method of processing the received FSK signal as set
2 forth in Claim 14 further comprising the step of deciding a logic
3 level of the third symbol as a function of: 1) a comparison of
4 the signal level of the center sample of the third symbol and the
5 mean signal level of the auto-correlation function and 2) a
6 comparison of the signal level of the center sample of the third
7 symbol and a signal level of a center sample of a fifth symbol
8 following the third symbol.

1 16. The method of processing the received FSK signal as set
2 forth in Claim 15 wherein the step of deciding the logic level
3 of the third symbol further comprises the step of deciding the
4 logic level of the third symbol as a function of a comparison of
5 the signal level of the center sample of the third symbol and the
6 signal level of the center sample of the first symbol.

1 17. The method of processing the received FSK signal as set
2 forth in Claim 16 further comprising the steps of:

3 in response to a determination that the second symbol,
4 the first symbol, and the third symbol comprise a 010 sequence
5 of symbols, comparing the signal level of the center sample of
6 the first symbol with 1) a signal level of a preceding sample of
7 the first symbol; and 2) a signal level of a following sample of
8 the first symbol; and

9
10 determining a location of a first positive-going peak
11 of the auto-correlation function corresponding to the first
12 symbol.

13
14 18. The method of processing the received FSK signal as set
15 forth in Claim 17 further comprising, in response to a
16 determination that the first positive-going peak does not
17 coincide with the center sample of the first symbol, the step of
18 one of:

19 advancing sampling of the baseband signal by at least
20 one sample time period; and

21 delaying sampling of the baseband signal by at least
22 one sample time period.

1 19. The method of processing the received FSK signal as set
2 forth in Claim 16 further comprising the steps of:

3 in response to a determination that the second symbol,
4 the first symbol, and the third symbol comprise a 101 sequence
5 of symbols, comparing the signal level of the center sample of
6 the first symbol with 1) a signal level of a preceding sample of
7 the first symbol and 2) a signal level of a following sample of
8 the first symbol; and

determining a location of a first negative-going peak of the auto-correlation function corresponding to the first symbol.

20. The method of processing the received FSK signal as set forth in Claim 19 further comprising, in response to a determination that the first negative-going peak does not coincide with the center sample of the first symbol, the step of one of:

advancing sampling of the baseband signal by at least one sample time period; and

delaying sampling of the baseband signal by at least one sample time period.